Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	530208	(electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:34
L2	11276	voltage adj regulation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:34
L3	1393	L1 and L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:35
L4	0	3 and (bias\$3 with parallel with sinking)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:37
L5	237414	nonvolatile or (non-volatile) or (non adj volatile) adj3 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:36
L6	713	2 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:36
L7	0	6 and (bias\$3 with parallel with sinking)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:38
L8	8	6 and (bias\$3 with sinking)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:45
L9	5	8 and @ad<="20030228"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:46

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L10	3	9 and (bit adj line)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 16:46
S1	480876	(electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 13:45
S2	10582	voltage adj regulation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:06
S3	1243	S1 and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 13:44
S4	14	S3 and (memory adj cell adj matrix)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 13:57
S5	69	S3 and (column adj decoder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 13:56
S6	78	S3 and ((memory adj cell adj matrix) or (column adj decoder))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:06
S7	36	(program\$3 adj load adj circuit) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:40
S8	3	(non adj volatile) and falsh	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/30 16:30

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S9	46361	(non adj volatile) and flash	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:05
S10	12273	S9 and eras\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 16:24
S11	11157	S10 and read\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 16:25
S12	8649	S11 and writ\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 16:25
S13	178	S12 and (program\$3 adj load)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 16:25
S14	110	S13 and ground	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 16:31
S15	69	S14 and active	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/18 16:27
S16	3	"6603681"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/30 16:30
S17	3	(non adj volatile) and falsh	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 14:30

S18	483783	(electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:04
S19	1206	365/185.21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:06
S20	24	S19 and (voltage adj regulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:06
S21	467	S19 and ((non adj volatile) and flash)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S22	158	S21 and ((memory adj cell adj matrix) or (column adj decoder))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:06
S23	7	S22 and (voltage adj regulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:06
S24	2314	365/185.18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S25	57	S24 and (voltage adj regulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S26	26	S25 and ((non adj volatile) and flash)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07

S27	777	365/185.19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S28	11	S27 and (voltage adj regulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S29	1156	365/185.09	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S30	455	S29 and ((non adj volatile) and flash)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S31	4	S30 and (voltage adj regulation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/01 15:07
S32	511570	(electrically adj3 memory) or (erasable adj3 memory) or (\$1eprom) or (flash or non-volatile or (non adj volatile) adj3 (memory or cell)) or ((non adj volatile) or nonvolatile or non-volatile) adj3 (memory or cell) or nvm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	, OR	ON	2006/04/19 17:38
S33	11019	voltage adj regulation	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:38
S34	1349	S32 and S33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:39
S35	57	S34 and (current adj sink\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:40

S36	45	S35 and @ad<="20030228"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:40
S37	0	S36 and (program\$3 adj load adj circuit) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:41
S38	0	((non adj volatile adj memory) and (voltage adj regulation adj system) and (multiword adj programming) and (corresponding adj circuits) and (memory adj cell adj content)and (current adj sinking)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:44
S39	21	martines-ignazio.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:44
S40	3	scardaci-massimo.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/19 17:45